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L32	L27 and l13	8	L32
L31	L27 and l12	0	L31
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L28	L27 and l6	153	L28
L27	L26 and retir\$7	178	L27
L26	l5 and (decod\$3 or predecod\$4)	210	L26
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L25	l4 and l13	10	L25
L24	l4 and l12	0	L24
L23	l4 and l10	55	L23

<u>L22</u>	l4 and l9	78	<u>L22</u>
<u>L21</u>	l4 and l8	116	<u>L21</u>
<u>L20</u>	l4 and l6	178	<u>L20</u>
<u>L19</u>	l3 and l13	17	<u>L19</u>
<u>L18</u>	l3 and l12	0	<u>L18</u>
<u>L17</u>	l3 and l10	63	<u>L17</u>
<u>L16</u>	l3 and l9	109	<u>L16</u>
<u>L15</u>	l3 and l8	150	<u>L15</u>
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<u>L13</u>	(718/102-108)[CCLS]	4445	<u>L13</u>
<u>L12</u>	(717/159-162)[CCLS]	955	<u>L12</u>
<u>L11</u>	(711/118-221)[CCLS]	25719	<u>L11</u>
<u>L10</u>	(711/118-221)![CCLS]	25719	<u>L10</u>
<u>L9</u>	(712/230-248)![CCLS]	3335	<u>L9</u>
<u>L8</u>	(712/205-219,225-228)[CCLS]	5731	<u>L8</u>
<u>L7</u>	(712/2-300)[CCLS]	12968	<u>L7</u>
<u>L6</u>	(712/2-300)![CCLS]	12968	<u>L6</u>
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<u>L5</u>	branch\$3 and l4	210	<u>L5</u>
<u>L4</u>	L3 and (reorder\$4 or order\$5 or rearrang\$7 out near3 (sequenc\$4 or order)) near25 (buffer\$4 or fifo or filo or lifo)	210	<u>L4</u>
<u>L3</u>	L2 and (predict\$5 or speculat\$5) near5 taken	281	<u>L3</u>
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1. A dynamic multithreading processor
Akkary, H.; Driscoll, M.A.;
[Microarchitecture, 1998. MICRO-31. Proceedings. 31st Annual ACM/IEEE International Symposium](#)
30 Nov.-2 Dec. 1998 Page(s):226 - 236
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IEE JNL IEE Journal or Magazine

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 1. **A case for fault tolerance and performance enhancement using chip multi-processors**

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[Computer Architecture Letters, IEEE](#)

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Golden, M.; Hesley, S.; Scherer, A.; Crowley, M.; Johnson, S.C.; Meier, S.; Meyer, D.; Moench, J.C.; Partovi, H.; Weber, F.; White, S.; Wood, T.; Yong, J.;

[Solid-State Circuits, IEEE Journal of](#)

Volume 34, Issue 11, Nov. 1999 Page(s):1466 - 1477

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 4. **A 7th-generation x86 microprocessor**

Hesley, S.; Andrade, V.; Burd, B.; Constant, G.; Correll, J.; Crowley, M.; Golden, M.; Hopkins, N.; It

Khondker, R.; Meyer, D.; Moench, J.; Partovi, H.; Posey, R.; Weber, F.; Yong, J.;

[Solid-State Circuits Conference, 1999. Digest of Technical Papers. ISSCC. 1999 IEEE International](#)

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